REMARKS

This application was originally filed on 11 August 2000 with three claims, all of which were written in independent form. Claims 4-9 were added on 4 August 2004. Claims 1-3 have been amended herein. No claims have been allowed.

Claims 1-9 were rejected under 35 U.S.C. § 102(a) as being anticipated by U.S. Patent No. 5,986,640 to Baldwin *et al.* ("Baldwin").

The Examiner stated, "Baldwin divides an image frame period into at least two refresh periods (figure 6 shows the division of sub-frames and column 7, starting from line 19 describes the figures; furthermore the 'reset' described in figure 7 and column 8, line 12 corresponds to the refresh requirement of the claimed invention)."

The pending independent claims have been amended to clarify the refresh periods and thereby distinguish the present invention from the prior art. Claims 1 and 2 have been amended to recite, "each refresh period comprising a period in which at least two image data bits are displayed." Claim 3 has been amended to recite, "each refresh period comprising a period in which at least two image bits are displayed."

The Examiner's interpretation of Baldwin is that each bit period is equivalent to a refresh period. The present claims distinguish over this interpretation of Baldwin by reciting at least two image (data) bits are displayed during each refresh period.

With respect to Claim 1, Baldwin does not show, teach, or suggest the limitation of "each refresh period comprising a period in which at least two image data bits are displayed" in combination with "displaying said first image data bit during some, but not all, of said refresh periods; and displaying said second image data bit during more of said refresh periods than said first image data bit."

With respect to Claim 2, Baldwin does not show, teach, or suggest the limitation of "each refresh period comprising a period in which at least two image data bits are displayed" in combination with "displaying each said image data bit in enough of said refresh periods to achieve said minimum temporal frequency, wherein not all of said image data bits are displayed in all of said refresh periods."

With respect to Claim 3, Baldwin does not show, teach, or suggest the limitation of "each

TI-28454 Amendment - Page 5

refresh period comprising a period in which at least two image data bits are displayed" in combination with "said processing allocating a series of refresh periods to said image bits such that not all of said image bits are displayed in the same number of said refresh periods."

Claims 4-9 depend from Claims 1-3 and should be deemed allowable for that reason and on their own merits. For the reasons expressed above with respect to the independent claims, the prior art does not show, teach, or suggest the combination of limitations recited by the independent claims, much less the combination of limitations recited by the independent claims in combination with the additional limitations of the dependent claims.

In view of the amendments and the remarks presented herewith, it is believed that the claims currently in the application accord with the requirements of 35 U.S.C. § 112 and are allowable over the prior art of record. Therefore, it is urged that the pending claims are in condition for allowance. Reconsideration of the present application is respectfully requested.

Respectfully submitted,

Cursil

Charles A. Brill Reg. No. 37,786

Texas Instruments Incorporated PO Box 655474 M/S 3999 Dallas, TX 75265 (972) 917-4379

FAX: (972) 917-4418